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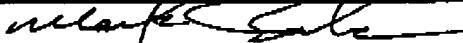
(to be used for all correspondence after initial filing)

		Application Number	10620286
		Filing Date	7/14/2003
		First Named Inventor	Om P Agrawal
		Art Unit	2819
		Examiner Name	Don P. Le
Total Number of Pages in This Submission	/1	Attorney Docket Number	LATC-01048US0

ENCLOSURES (Check all that apply)

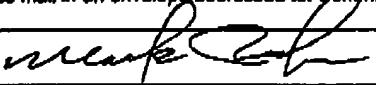
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<input type="checkbox"/> Affidavits/declaration(s)	<input type="checkbox"/> Power of Attorney, Revocation	<input type="checkbox"/> Status Letter
<input type="checkbox"/> Extension of Time Request	<input type="checkbox"/> Change of Correspondence Address	<input type="checkbox"/> Other Enclosure(s) (please identify below):
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name	LATTICE SEMICONDUCTOR CORPORATION		
Signature			
Printed name	MARK L BECKER		
Date	2/24/05	Reg. No.	31325

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First Named Inventor: Om P. Agrawal

Application No.: 10/620,286

Filed: 7/14/2003

For: FIELD PROGRAMMABLE GATE
ARRAY HAVING EMBEDDED
MEMORY WITH CONFIGURABLE
DEPTH AND WIDTH (as amended)

Examiner: Don P. Le

Art Unit: 2819

Attorney Docket No.: LATC-01049US0

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RESPONSE

Applicants respond to the Office action mailed 12/13/2004 as follows.